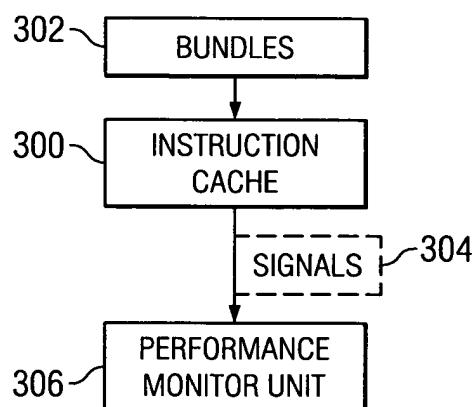
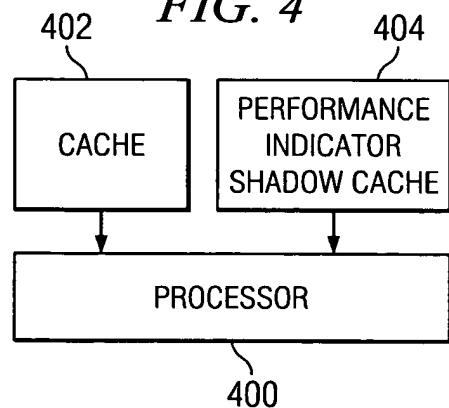
**FIG. 3****FIG. 4**

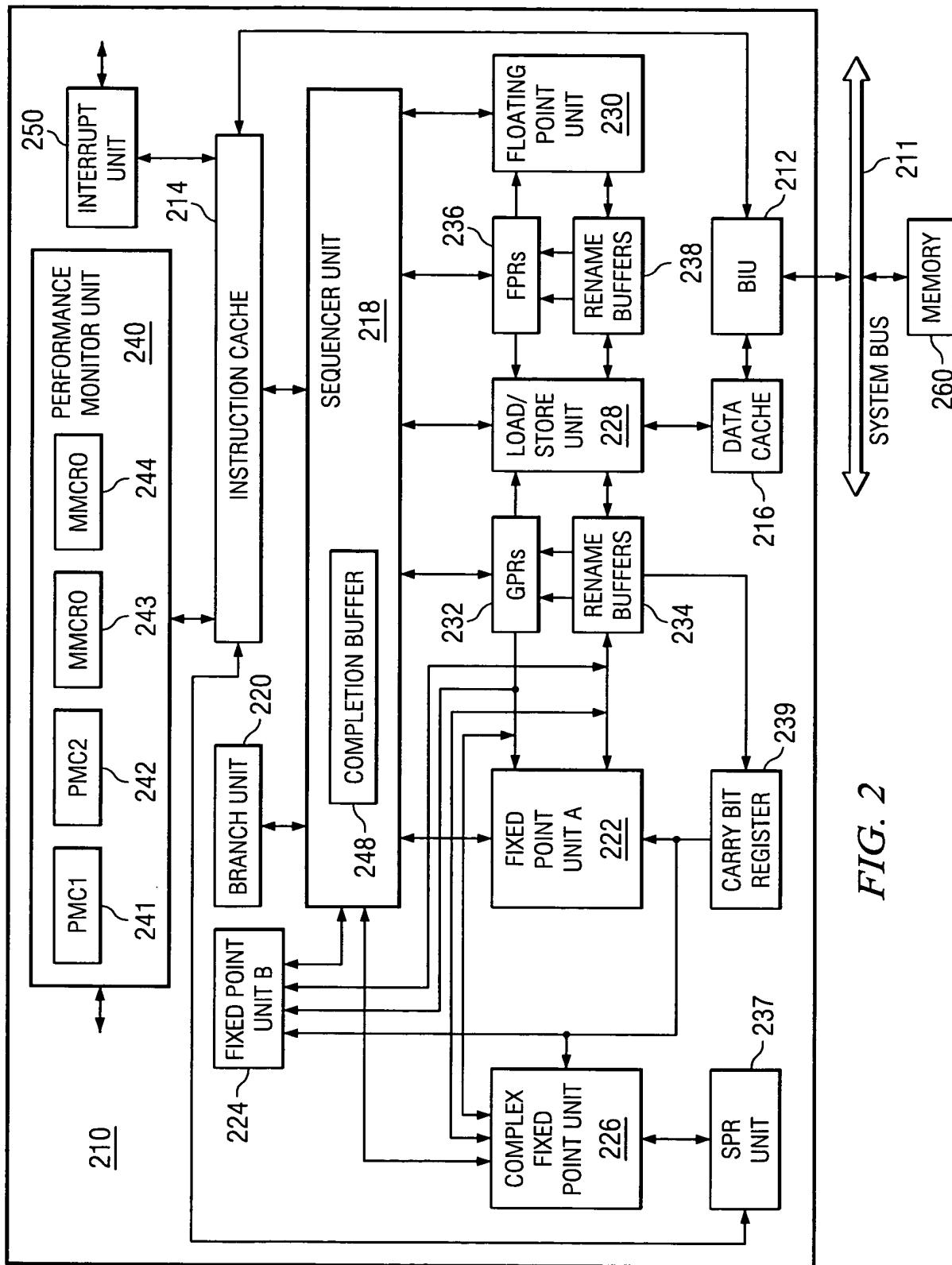
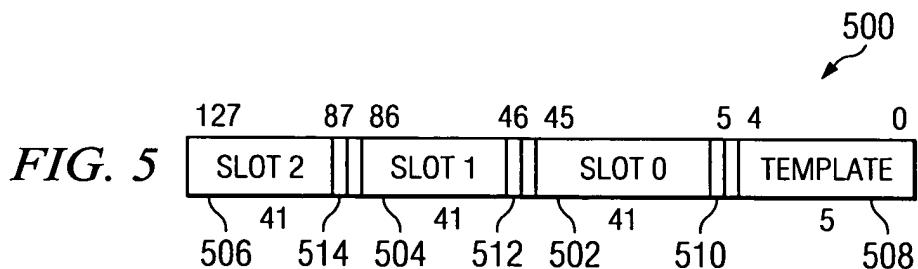
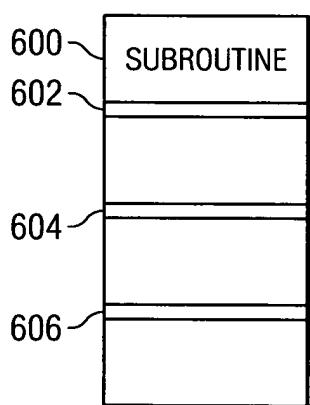
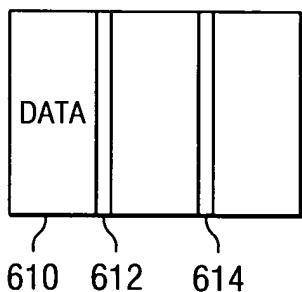


FIG. 2

*FIG. 6A**FIG. 6B*

500

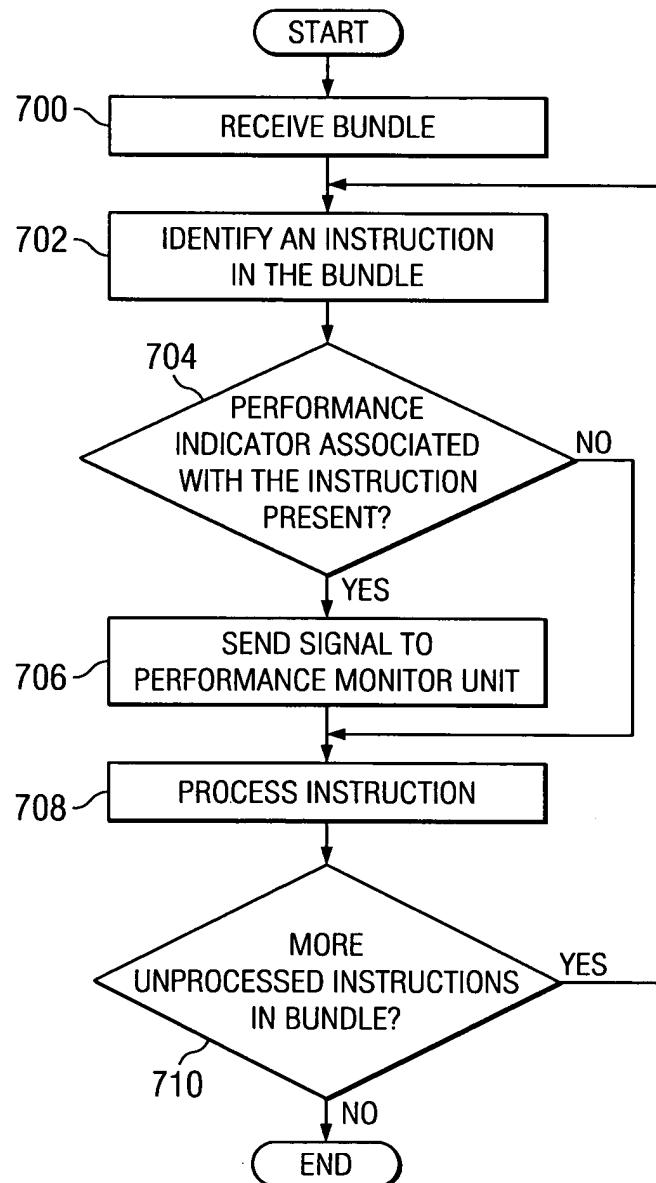
FIG. 7

FIG. 8

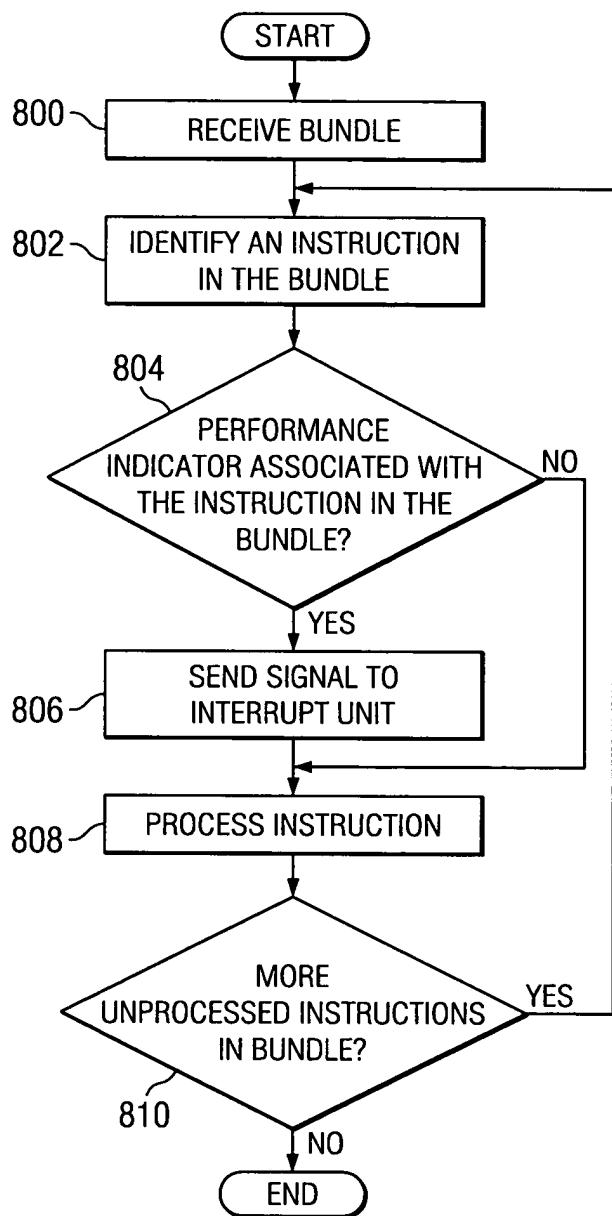


FIG. 9

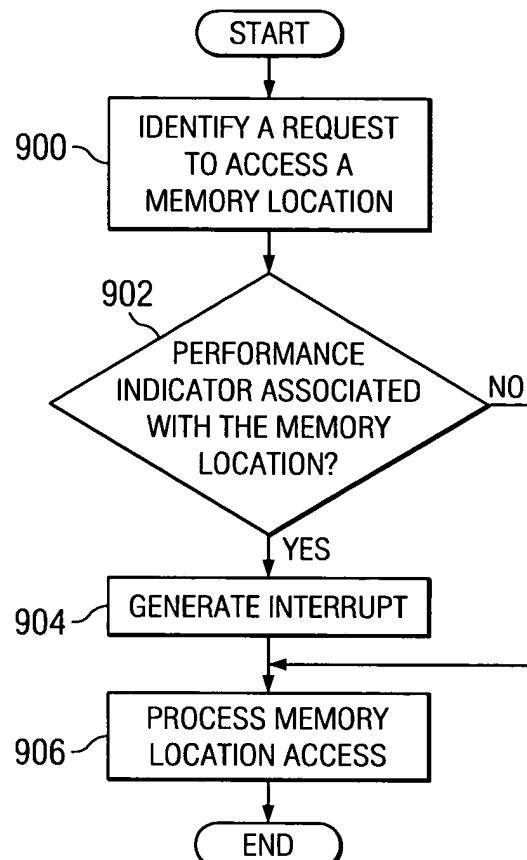


FIG. 10

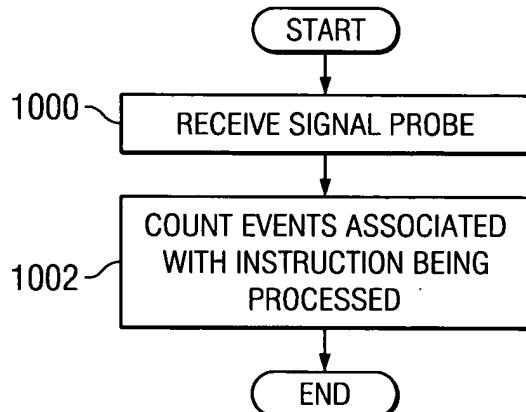


FIG. 11

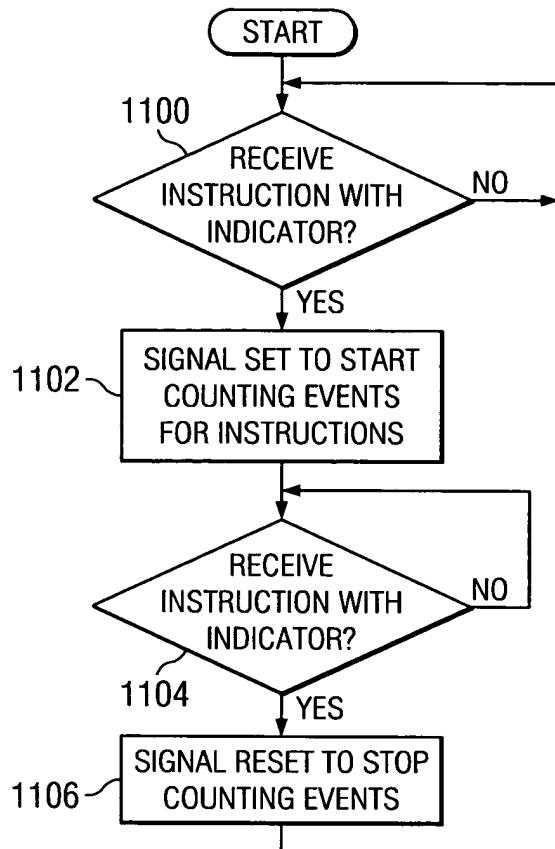


FIG. 13

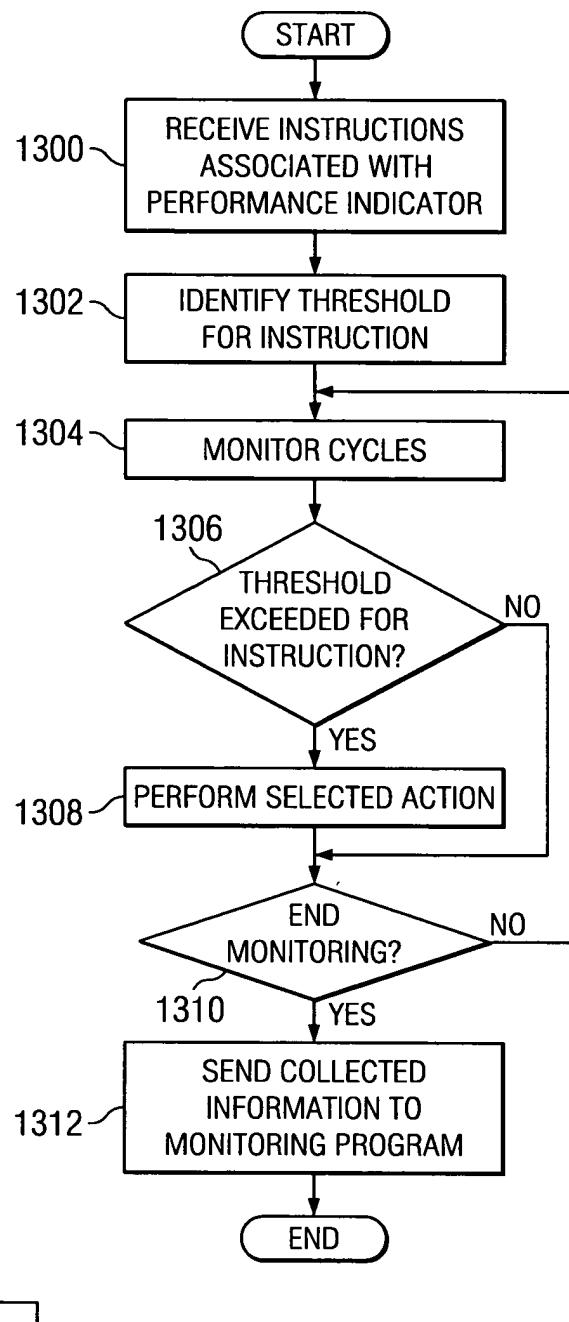


FIG. 12

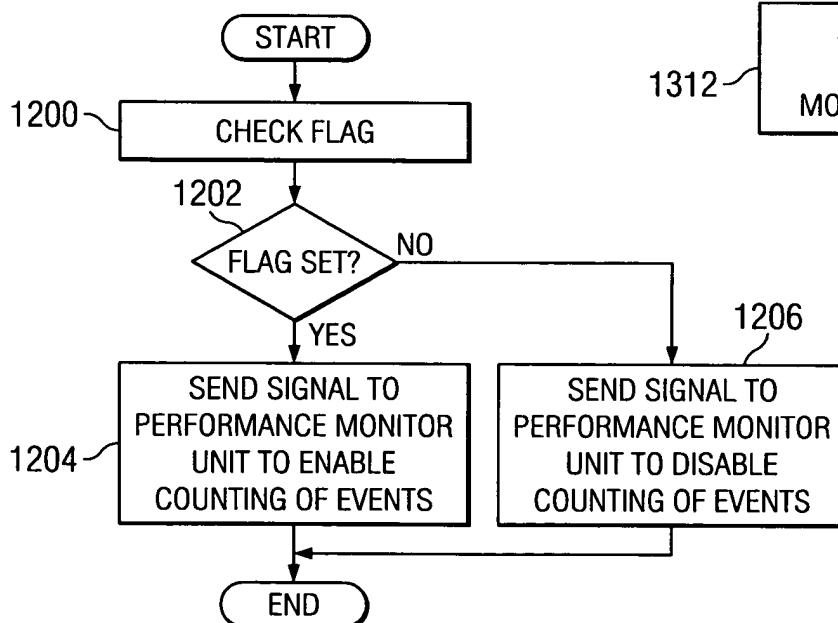


FIG. 14

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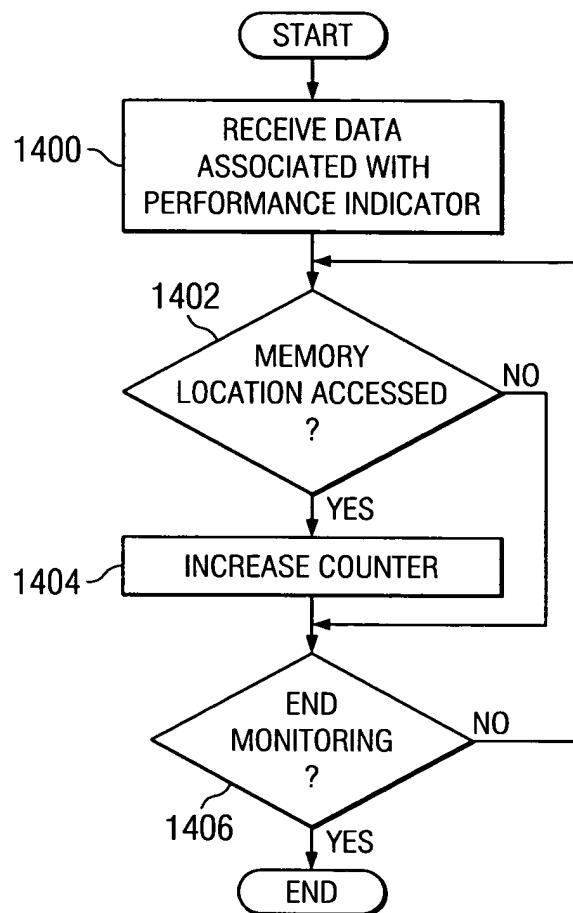


FIG. 18

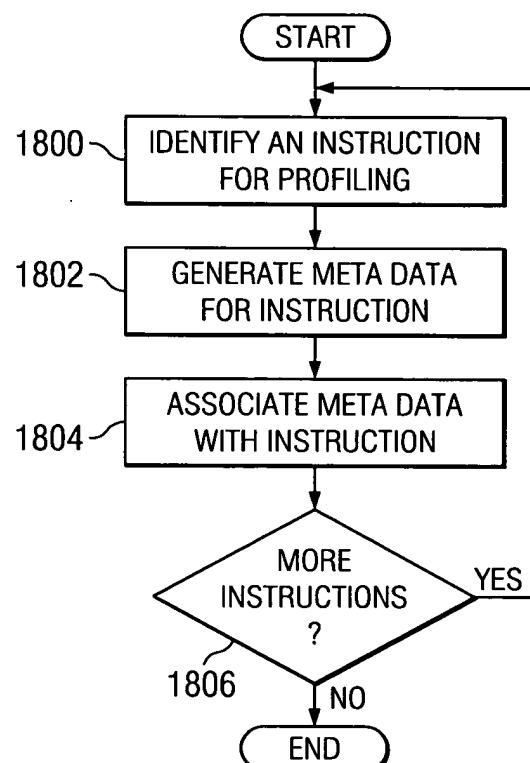


FIG. 15

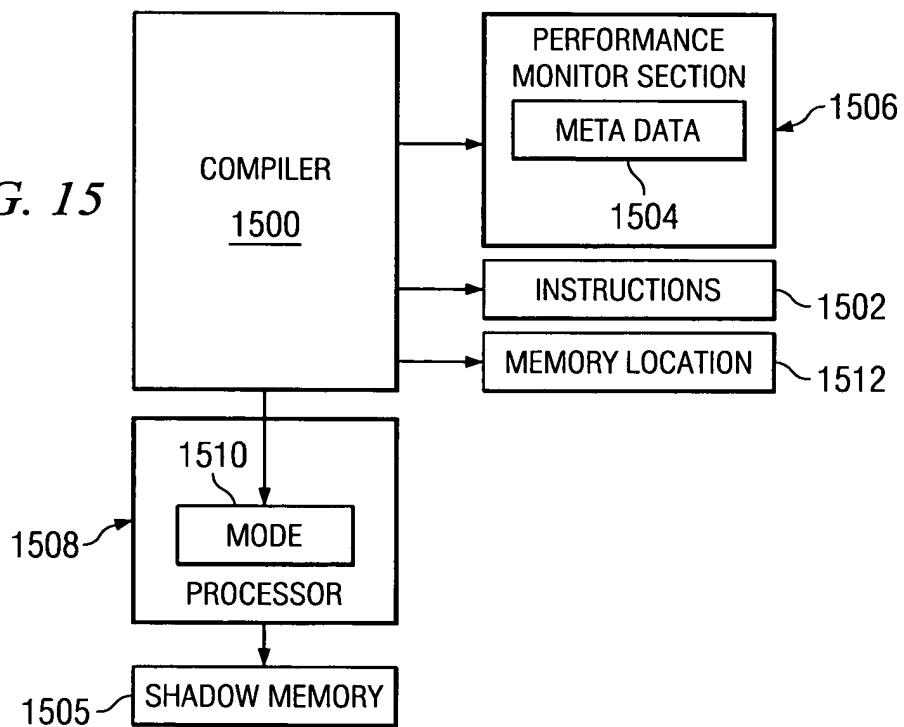


FIG. 16

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1612	# OF ENTRIES	.TEXT 1	1600
1602	ENTRY 1 OFFSET	5	
	ENTRY LENGTH	0	1616
	FLAGS	120	
1604	ENTRY 2 OFFSET	COUNT ALL INSTRUCTIONS IN THIS RANGE	1614
	ENTRY LENGTH	124	
	FLAGS	4	1618
1606	ENTRY 3 OFFSET	TAKE EXCEPTION	
	ENTRY LENGTH	160	
	FLAGS	4	1620
1608	ENTRY 4 OFFSET	THRESHOLD, GRANULARITY 100 CYCLES	
	ENTRY LENGTH	256	1622
	FLAGS	4	
1610	ENTRY 5 OFFSET	START TRACING	
	ENTRY LENGTH	512	1624
	FLAGS	4	

FIG. 17

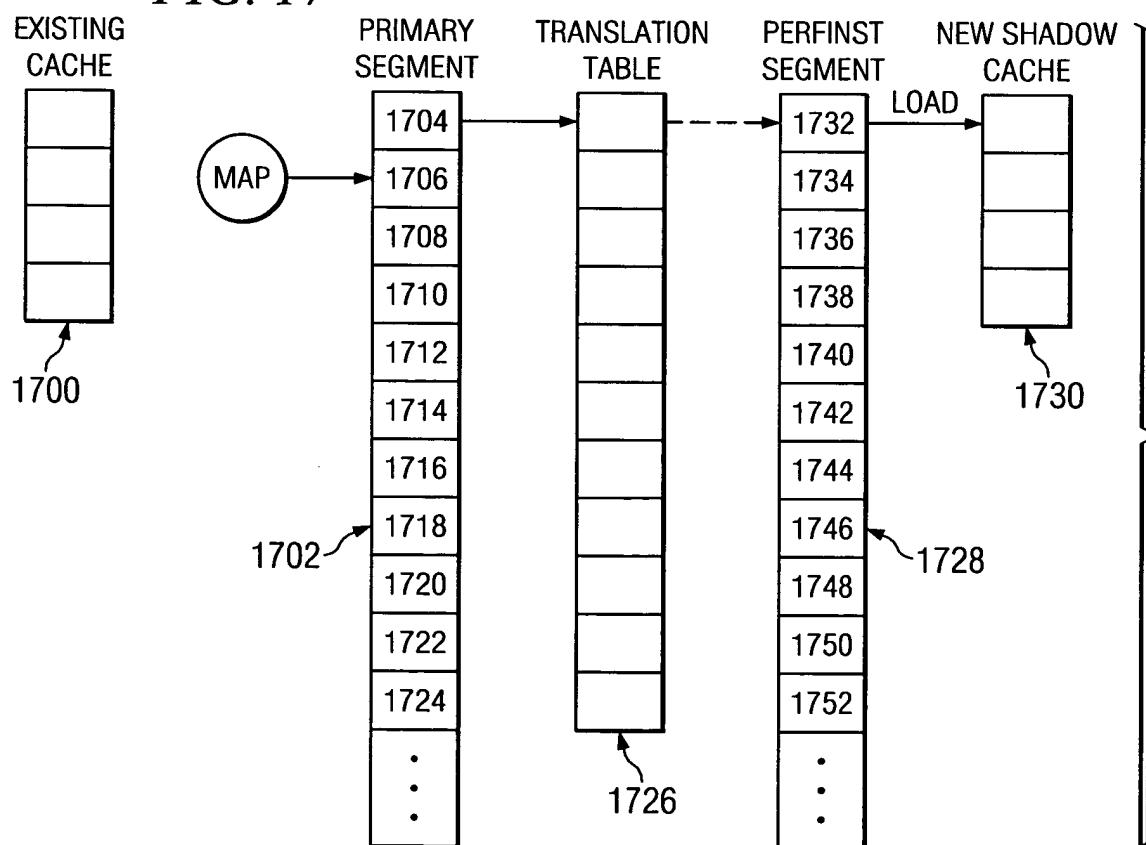


FIG. 19

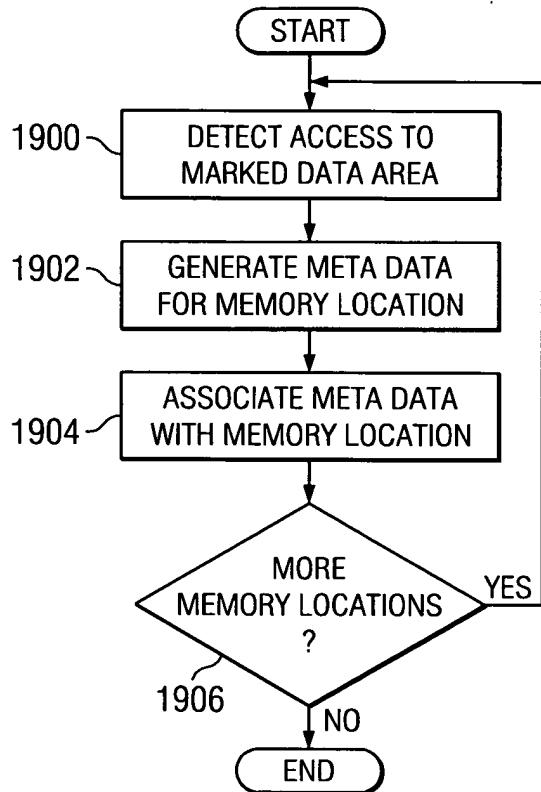


FIG. 20

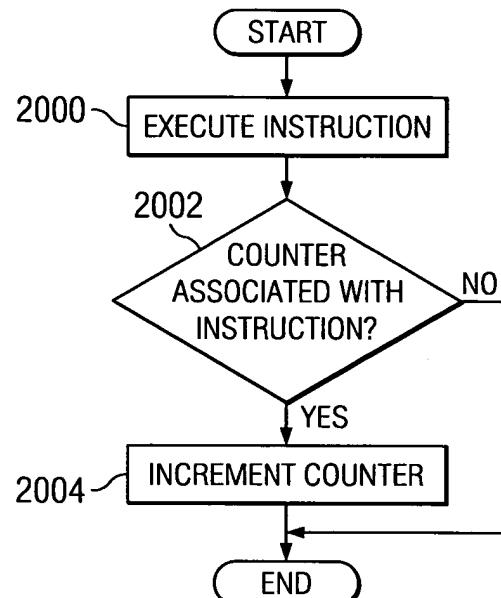
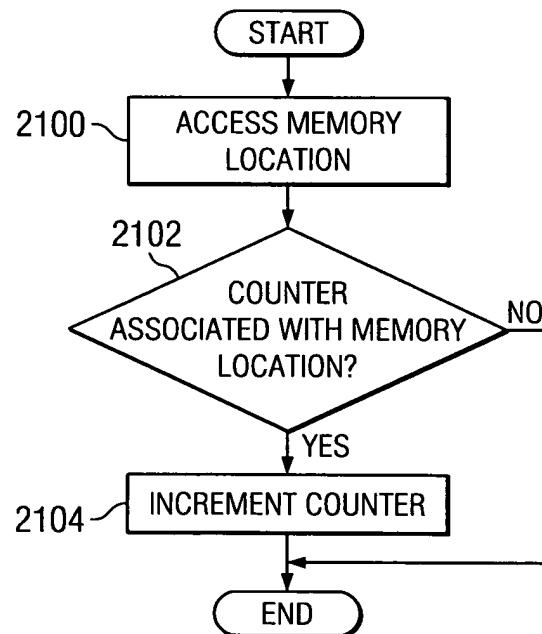


FIG. 21



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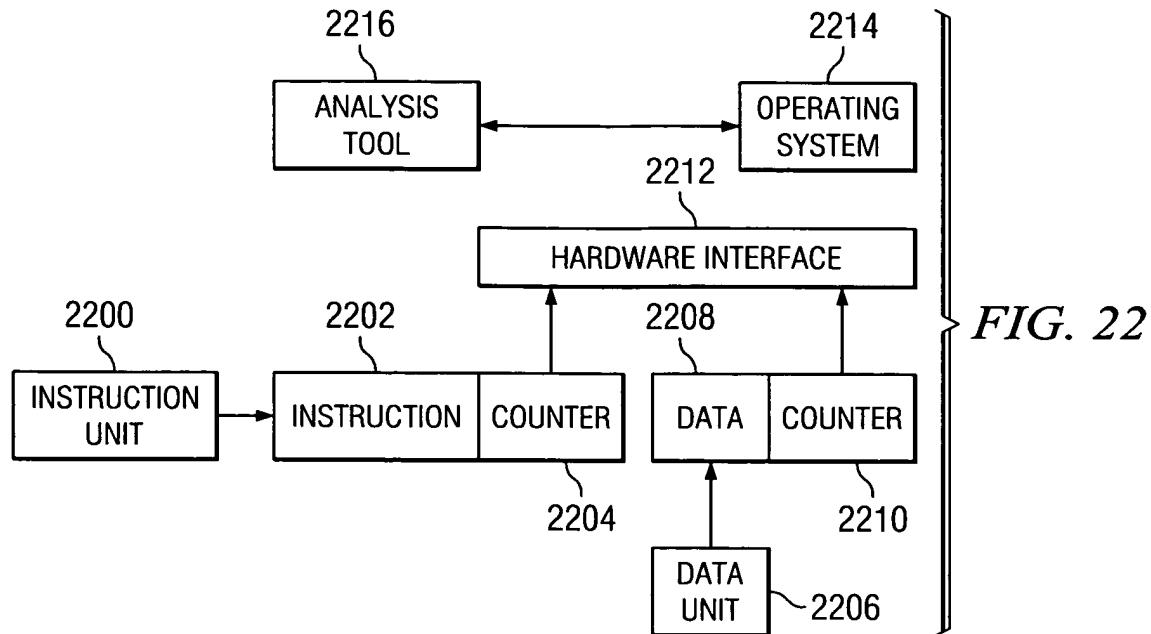


FIG. 24

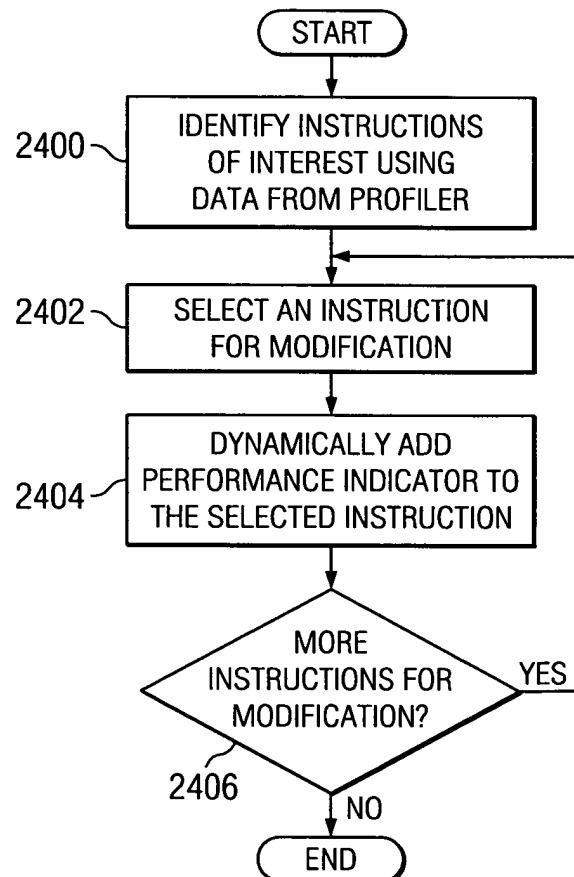


FIG. 23

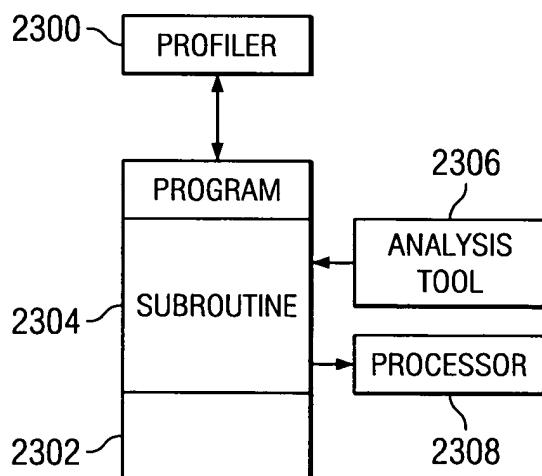


FIG. 25

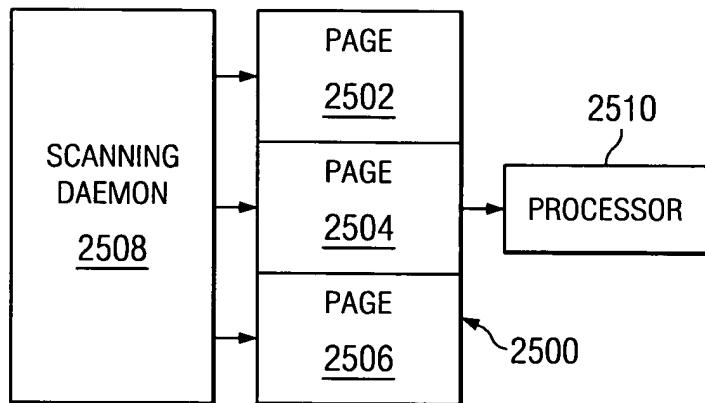


FIG. 26

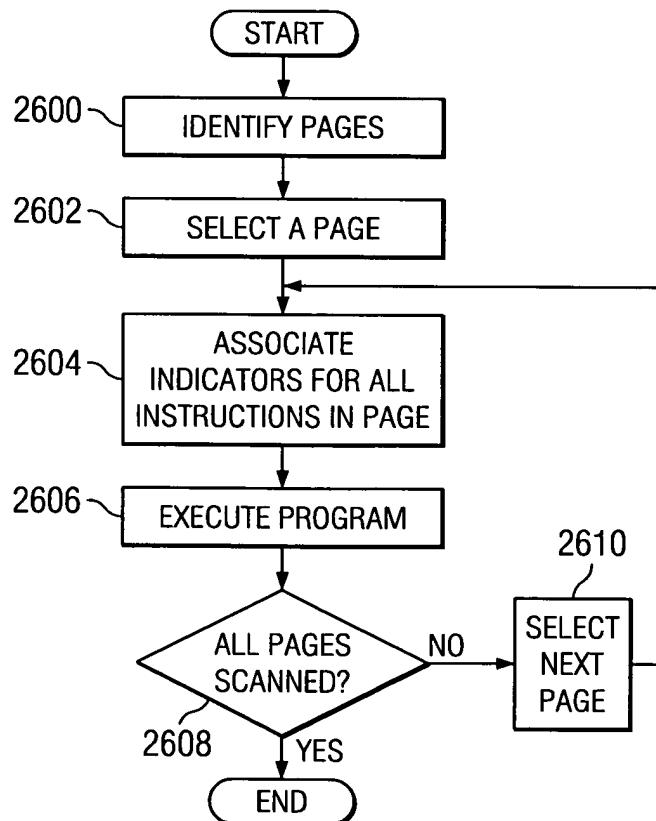


FIG. 27

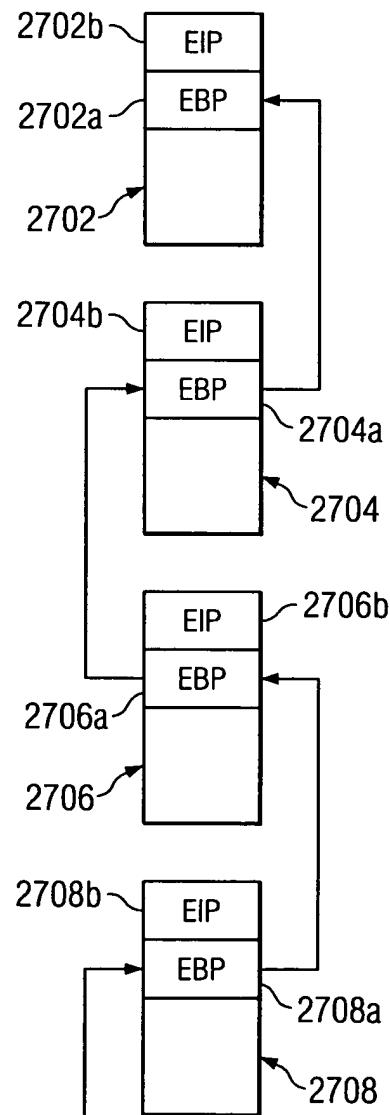


FIG. 28

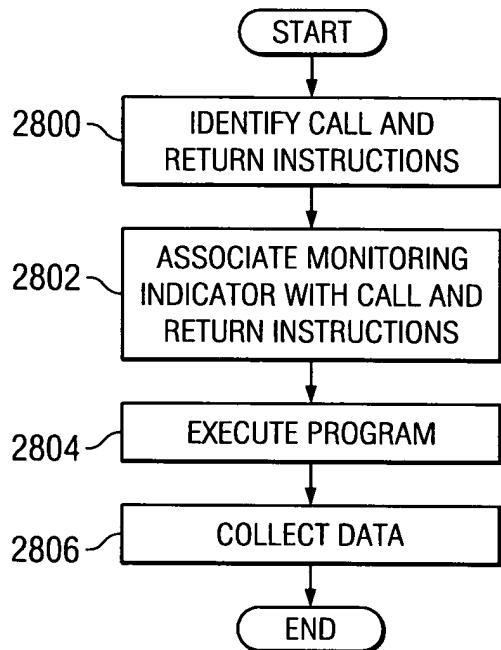


FIG. 30

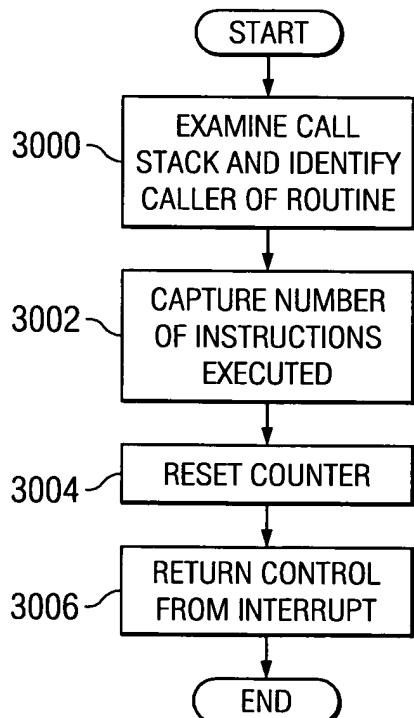
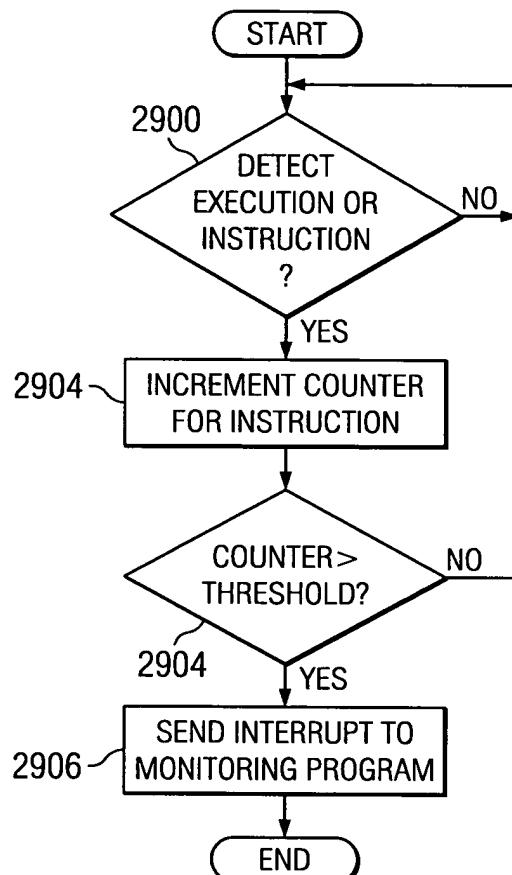


FIG. 29



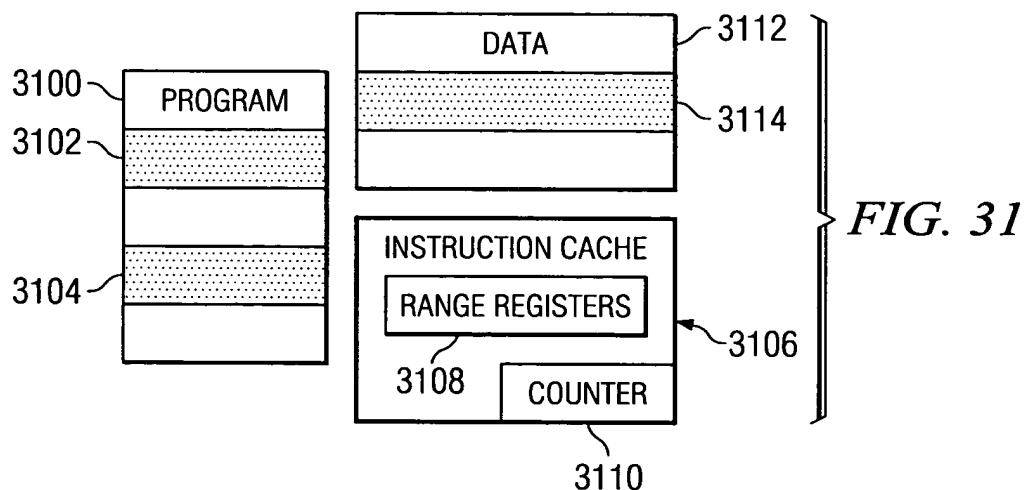
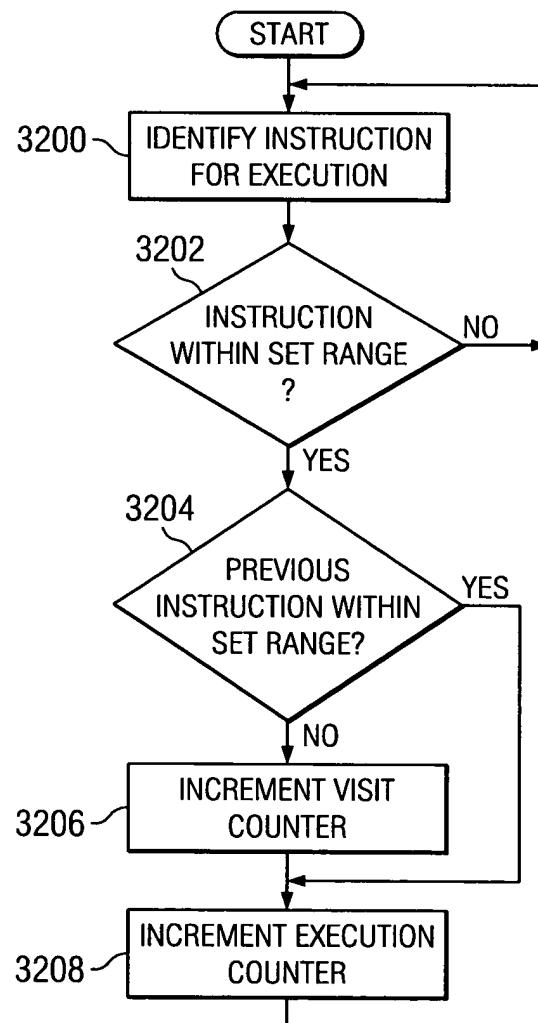


FIG. 32



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FIG. 33

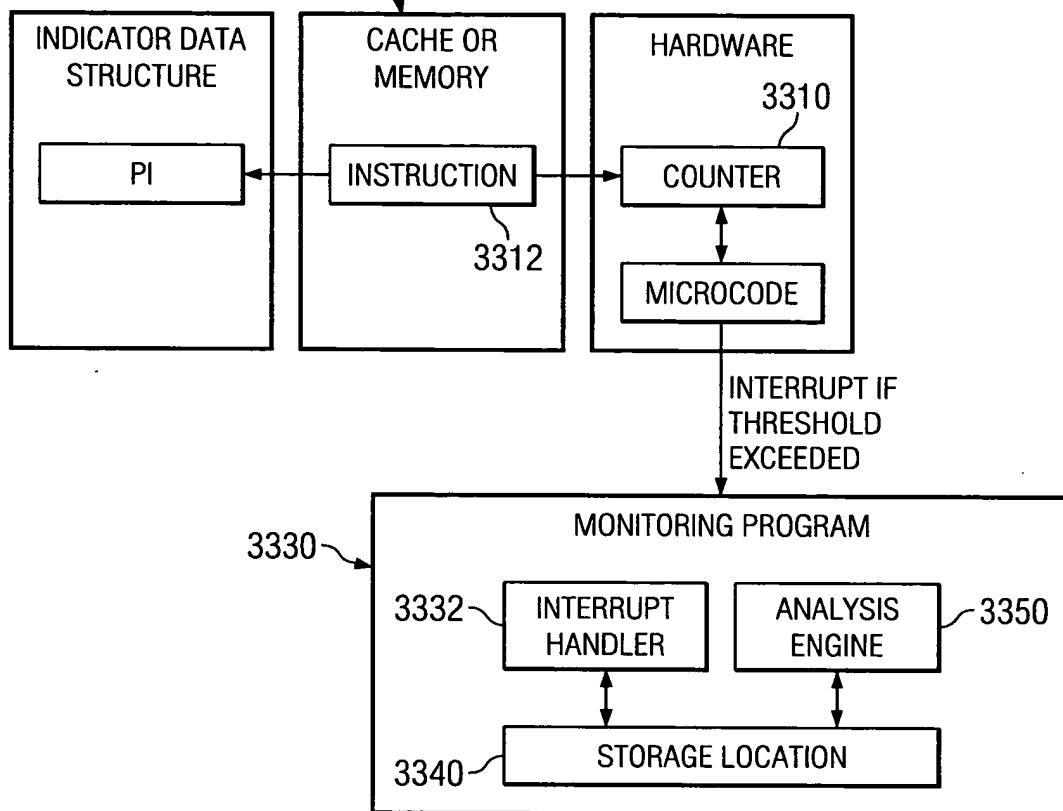


FIG. 37

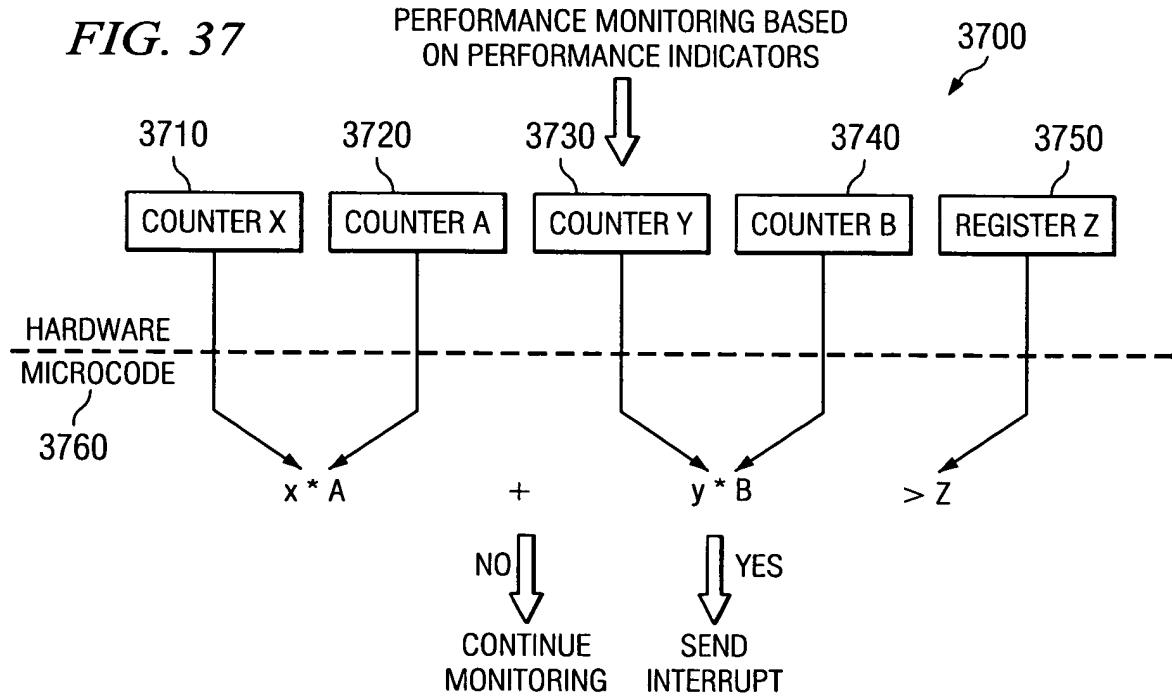


FIG. 34

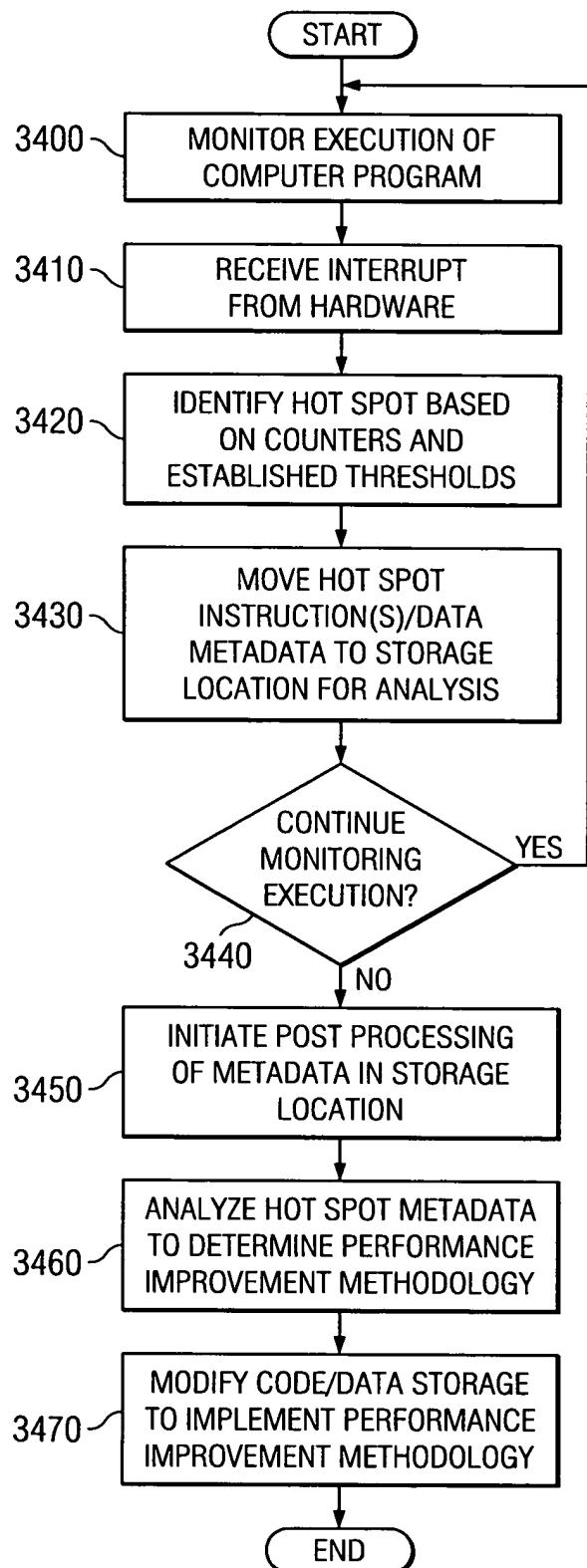


FIG. 35

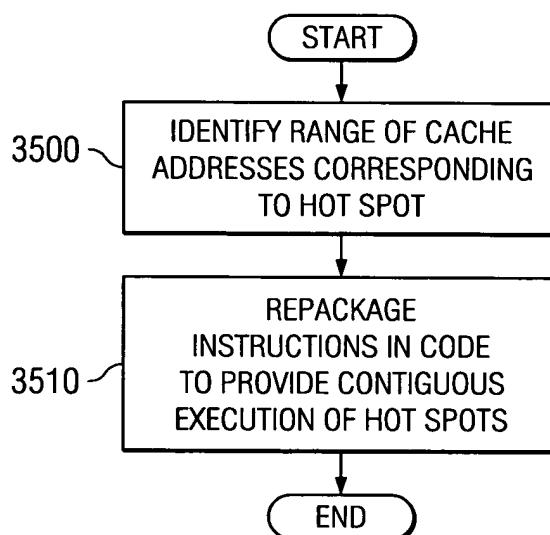


FIG. 36

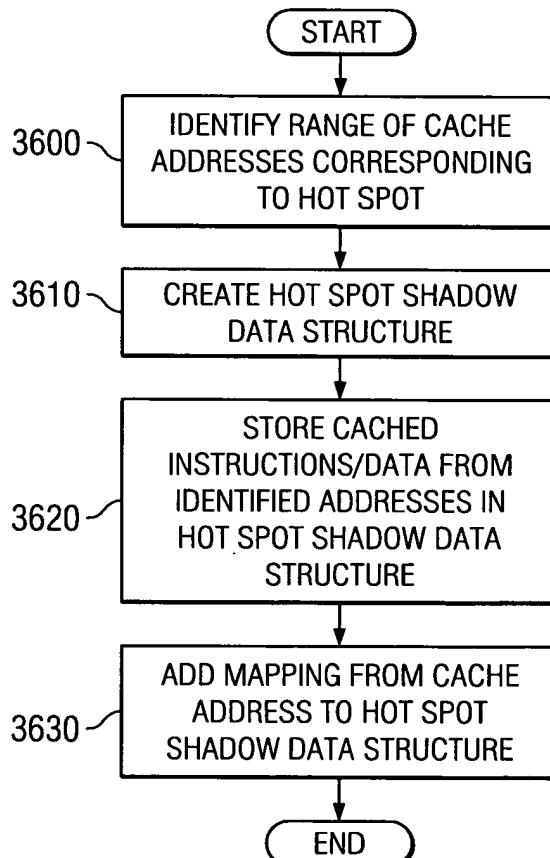


FIG. 38

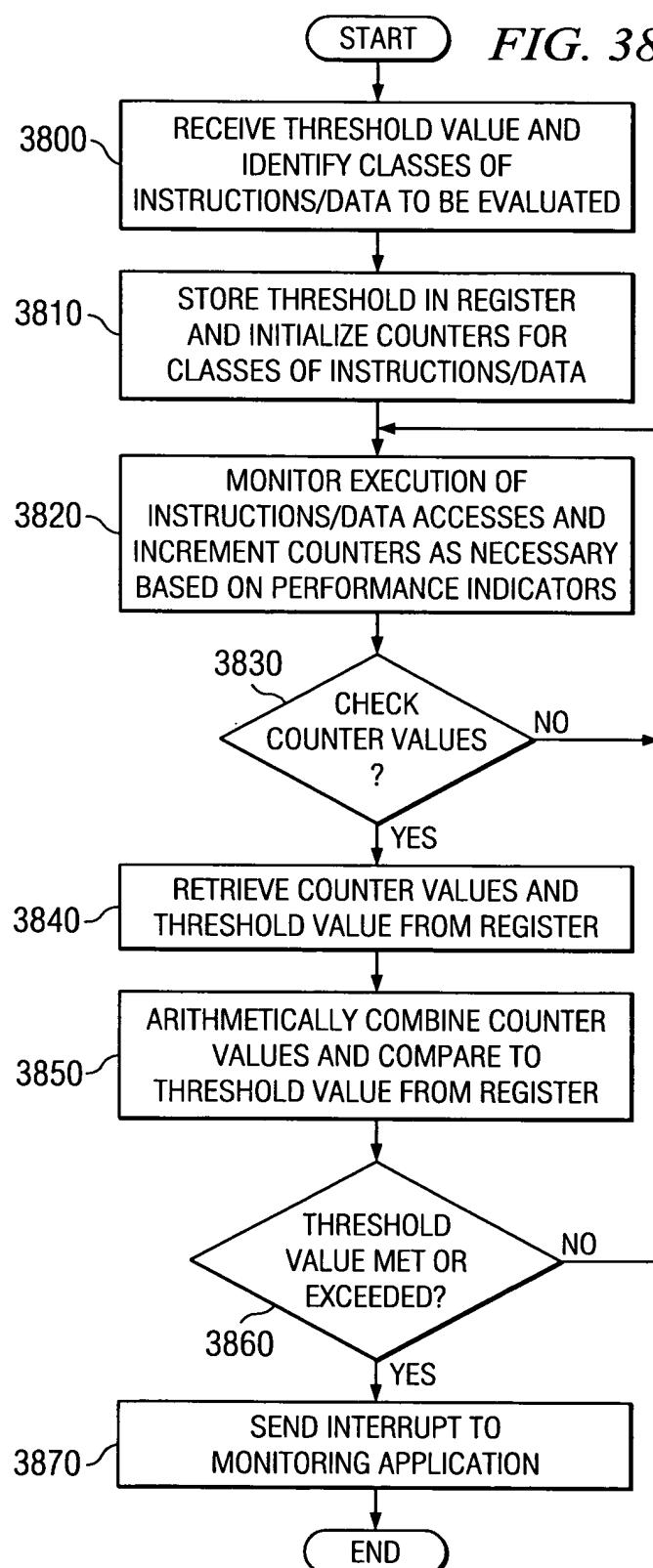


FIG. 39

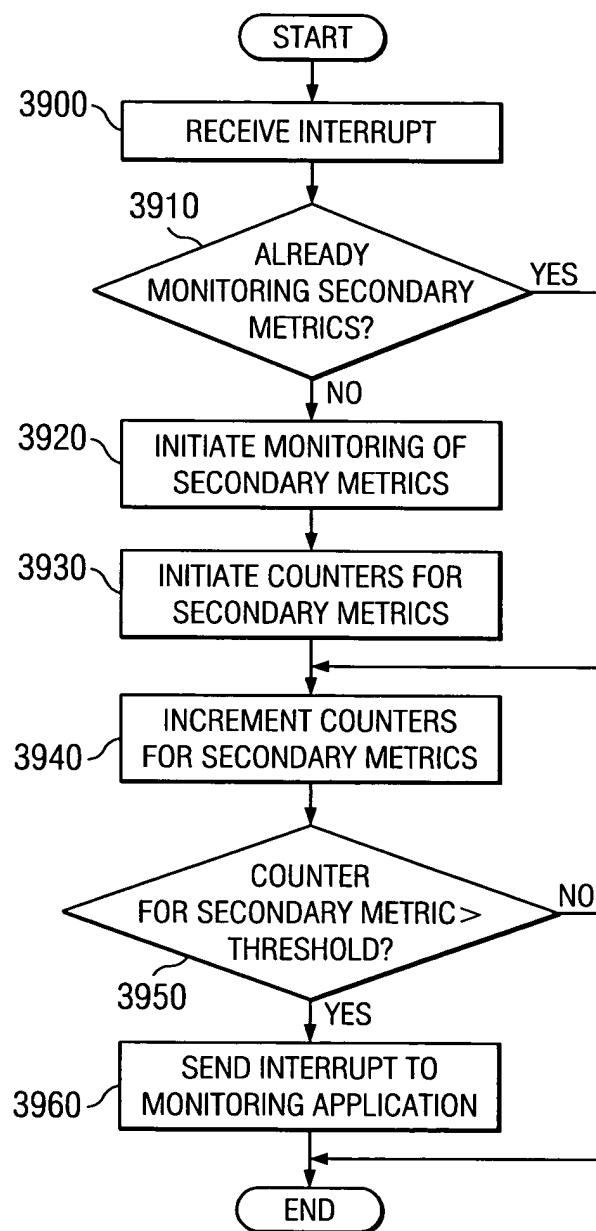


FIG. 40

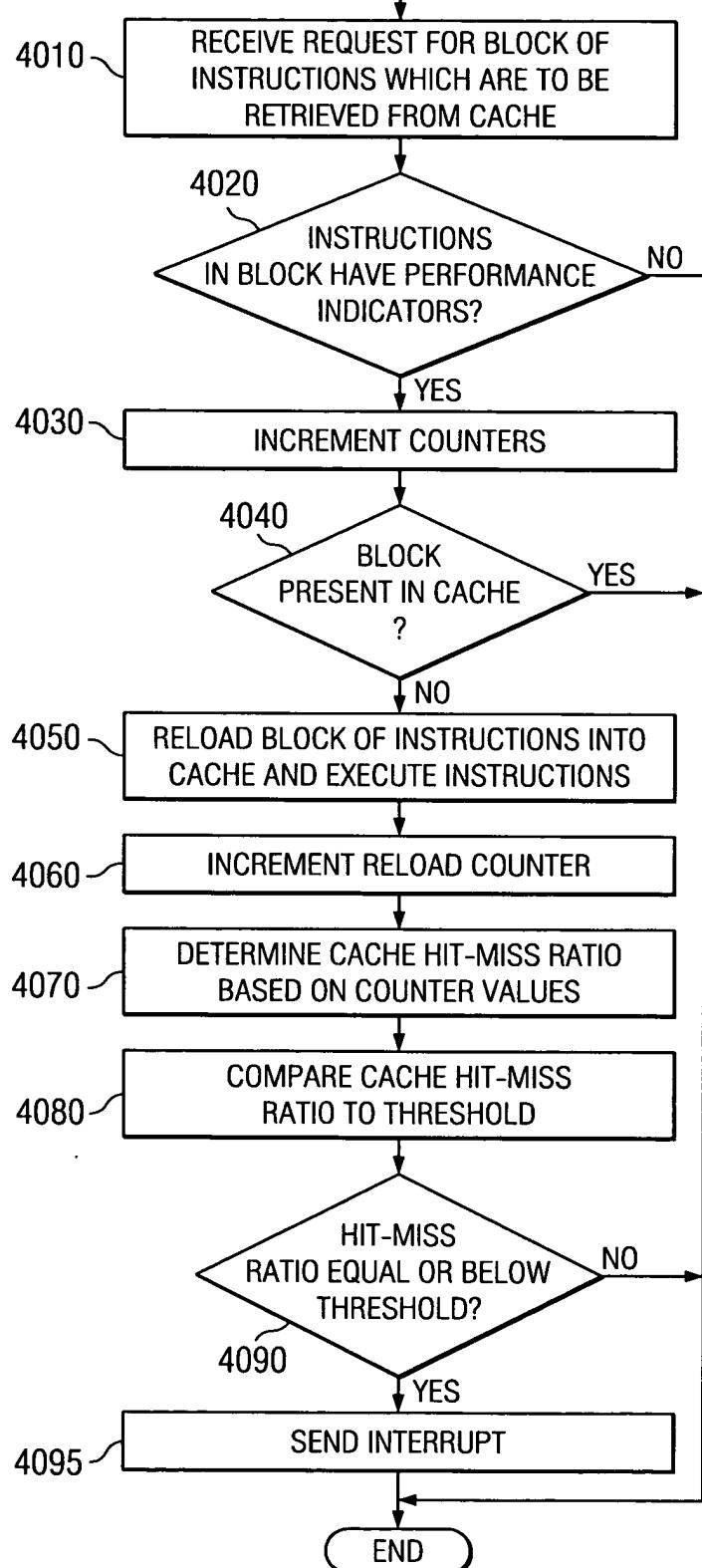
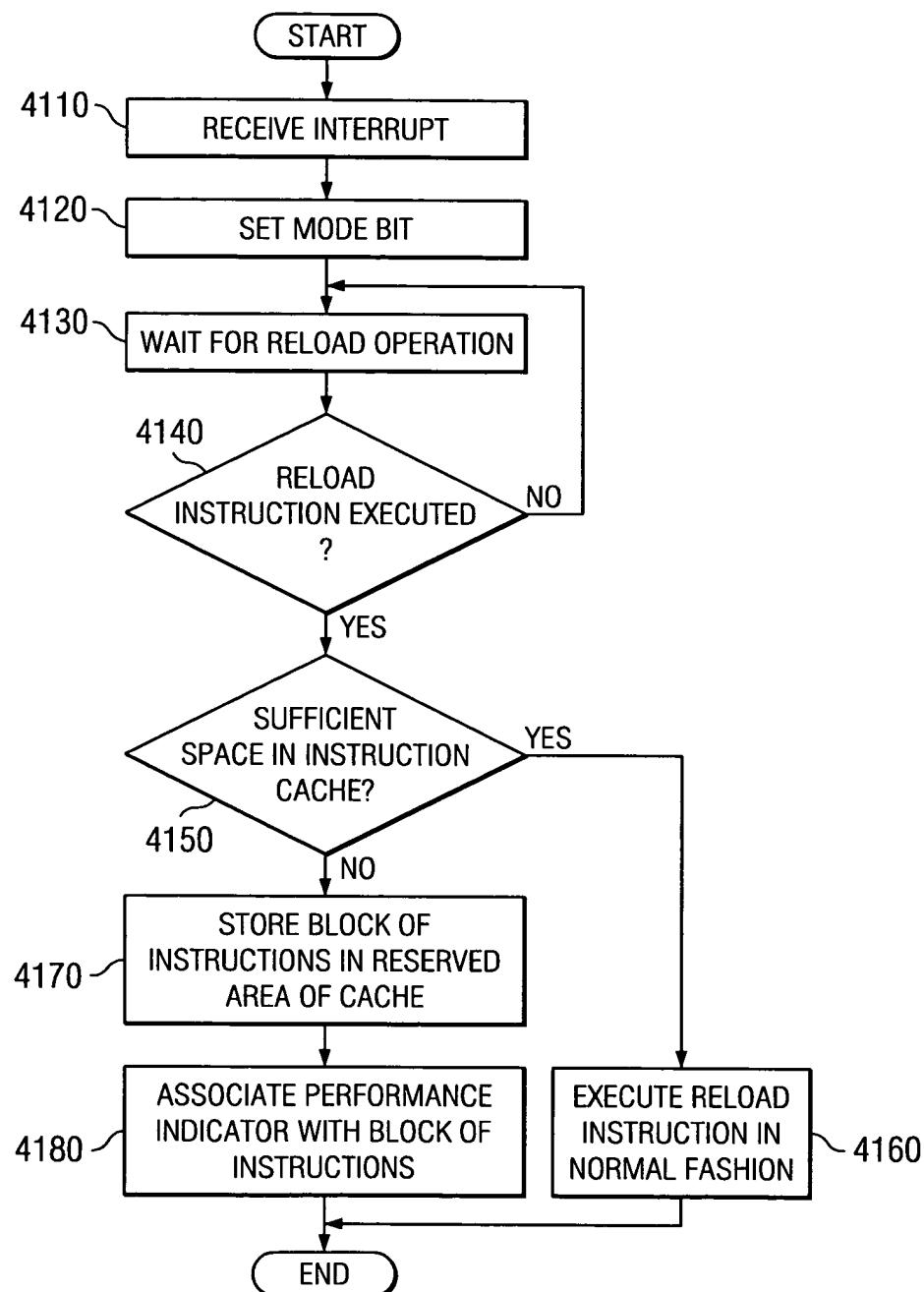


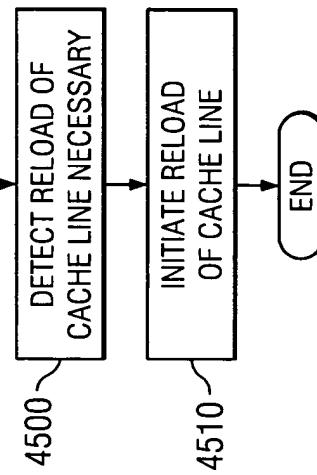
FIG. 41



INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT	
4210		4270		4290		4220		4272		4291	
INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT	
4230		COUNTS		4280		COUNTS		4280		COUNTS	
INSTRUCTION/ DATA AREA		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT	
4240		4274		4293		4250		4276		4295	
INSTRUCTION/ DATA AREA		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT	
4240		4274		4293		4250		4276		4295	
INSTRUCTION/ DATA AREA		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT	
4240		4274		4293		4250		4276		4295	
INSTRUCTION/ DATA AREA		INSTRUCTION/ DATA AREA		WRITE FLAG BIT		READ FLAG BIT		INSTRUCTION/ DATA AREA		WRITE FLAG BIT	
4240		4274		4293		4250		4276		4295	

FIG. 42

FIG. 45



4300	P1 WRITE FLAG BIT	P2 WRITE FLAG BIT	P3 WRITE FLAG BIT	...	Pn WRITE FLAG BIT
4310	4320	4330			4340

FIG. 43

4300

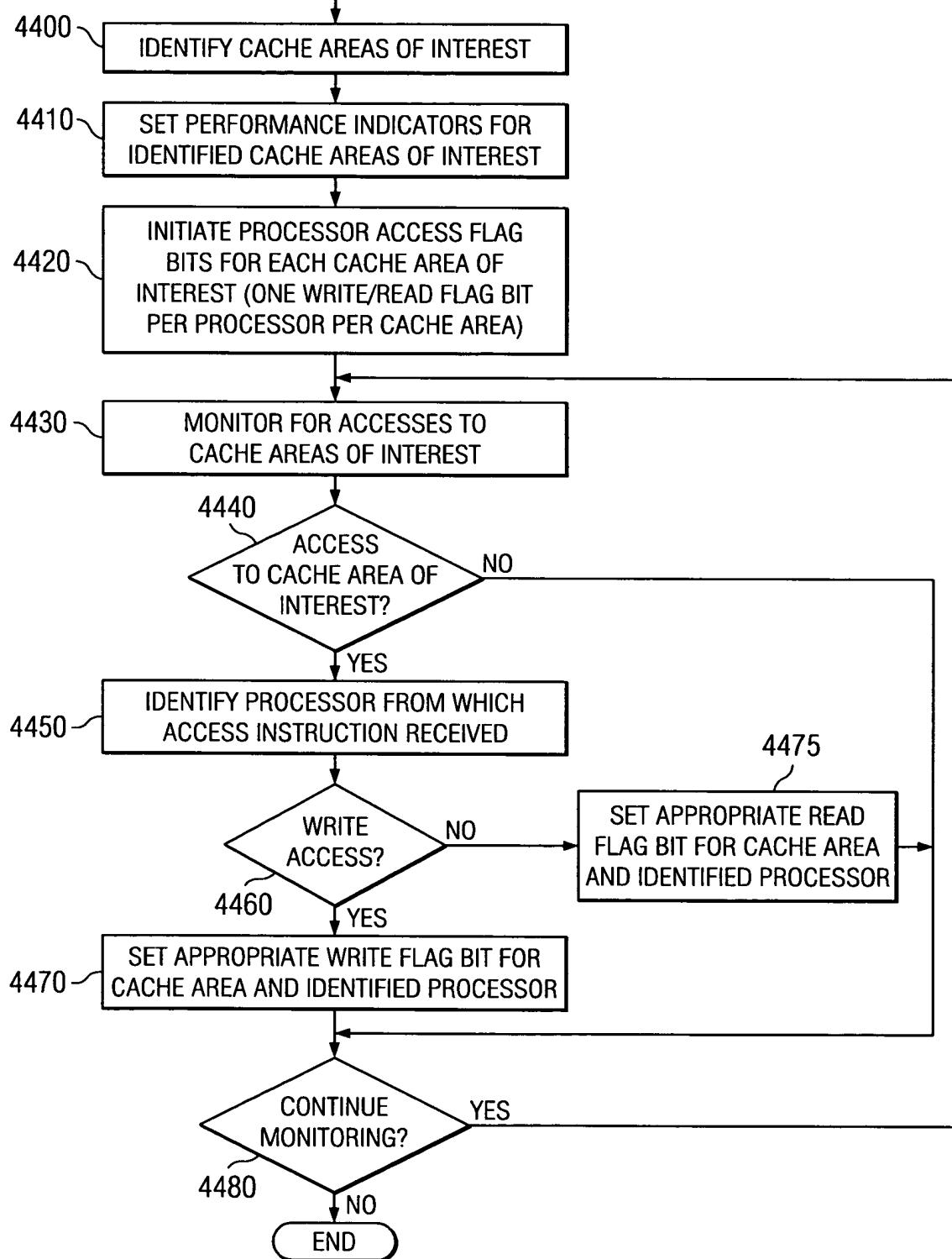
4310	4320	4330			4340
4310	4320	4330			4340

4310

4310	4320	4330			4340
4310	4320	4330			4340

4310

FIG. 44 START



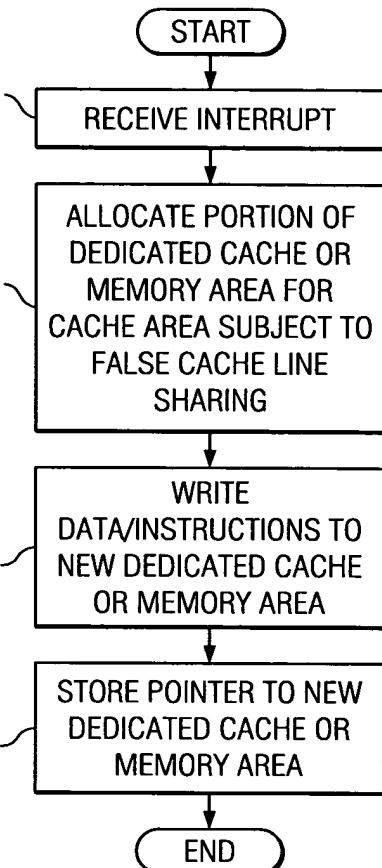
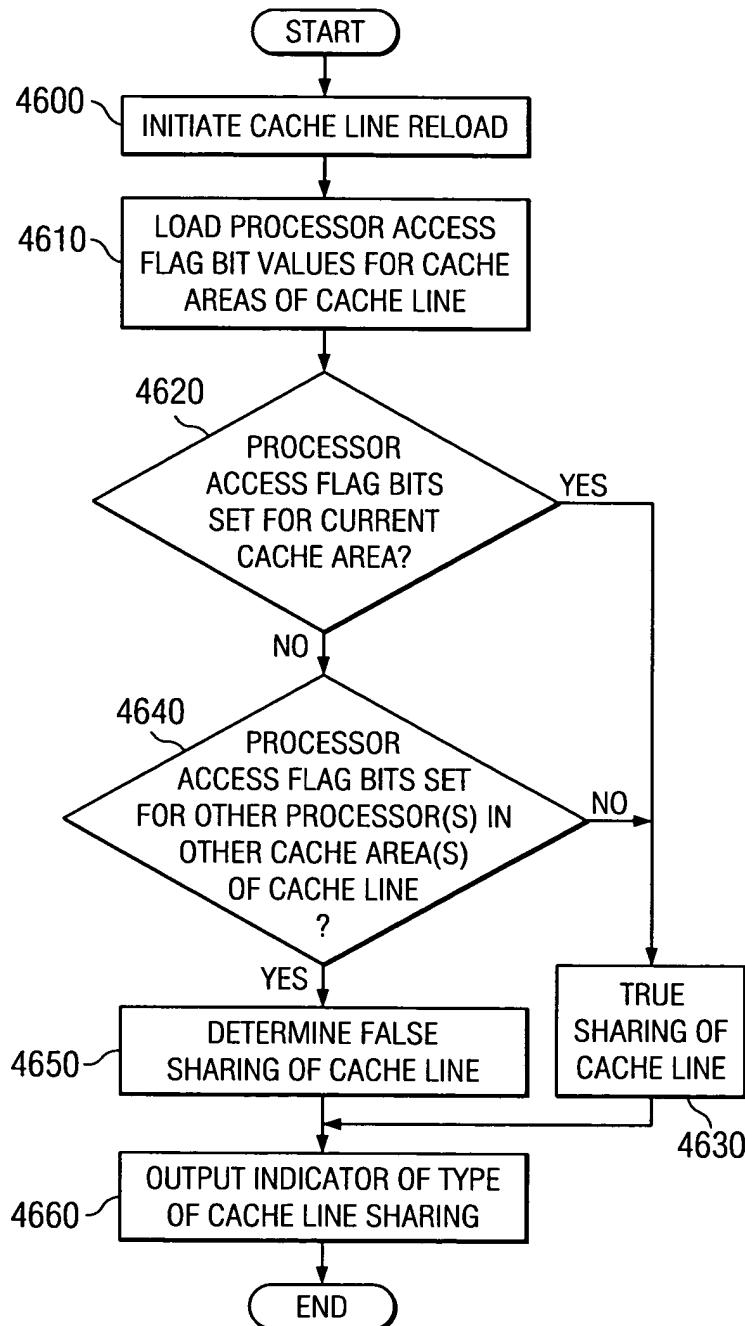


FIG. 46



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START

FIG. 48

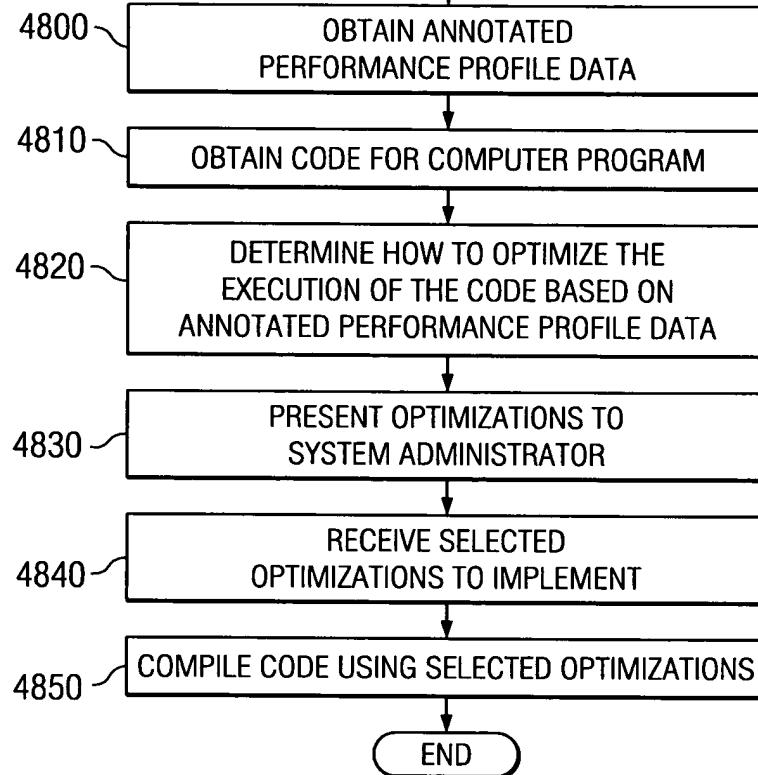


FIG. 49

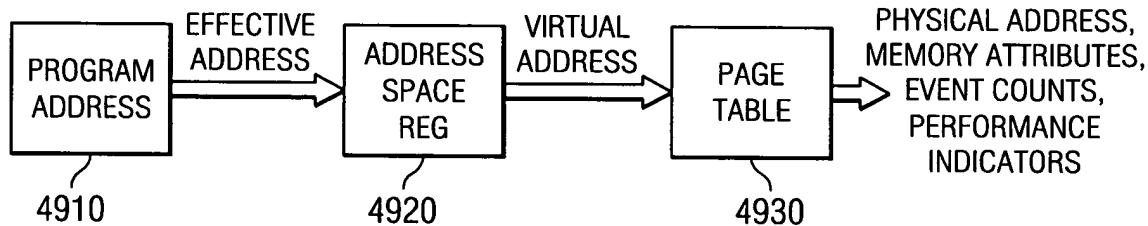


FIG. 50

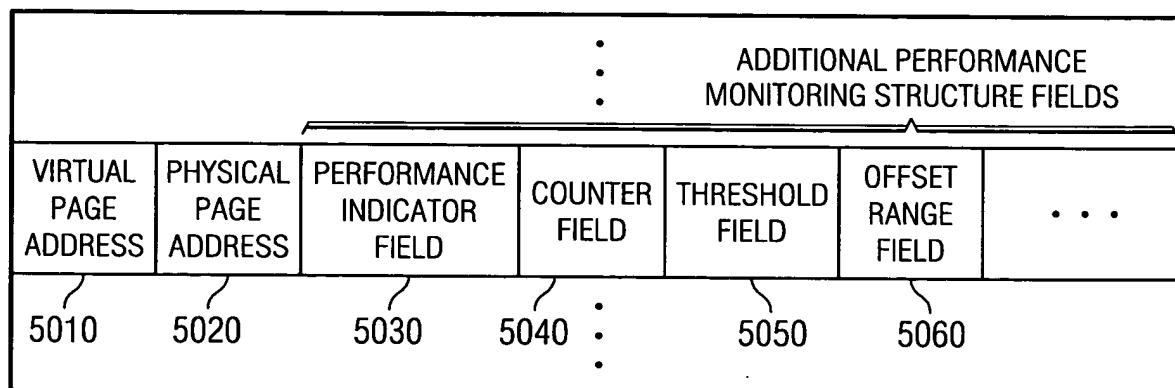


FIG. 51

